

LOW PROFILE STACKED MULTI-CHIP PACKAGE AND METHOD OF  
FORMING SAME

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ABSTRACT

A stacked multi-chip package includes first chip with conductive pads on both front and back sides. The front side may include a polymer layer with interconnect. A first polymer layer formed on the backside of the first chip has a cutout to receive a second chip. The first and second chip may be joined as a flip 10 chip. A second polymer layer formed on the first polymer layer has a cutout to receive a third chip. A third polymer layer formed on the second polymer layer contains interconnect to interconnect the first, second and third chips, including the backside of the first chip. Conductive bumps on the front side of the first chip and on the polymer layers provide external I/O connection.

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